

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. *Cancelled.*

2. *(previously presented)* The power amplification circuit according to Claim 3, wherein the impedance of the negative feedback circuit increases with an increasing input signal power to the power amplifier.

3. *(currently amended)* A power amplification circuit comprising:

a power amplifier; and

a negative feedback circuit connected between a power signal input terminal and a power signal output terminal of the power amplifier,

wherein impedance of the negative feedback circuit depends on a signal voltage occurring across the negative feedback circuit, and

wherein the negative feedback circuit is a series connection circuit consisting essentially of a diode and a capacitance device

connected in series, wherein the diode has a variable impedance characteristic.

4. (*currently amended*) A power amplification circuit comprising:

a power amplifier; and

a negative feedback circuit connected between a power signal input terminal and a power signal output terminal of the power amplifier,

wherein impedance of the negative feedback circuit depends on a signal voltage occurring across the negative feedback circuit, and

wherein the negative feedback circuit is a series connection circuit consisting essentially of a diode, a capacitance device and a feedback resistor connected in series, wherein the diode has a variable impedance characteristic.

5. (*currently amended*) A power amplification circuit comprising:

a power amplifier; and

a negative feedback circuit connected between a power signal input terminal and a power signal output terminal of the power amplifier,

wherein impedance of the negative feedback circuit depends on a signal voltage occurring across the negative feedback circuit, and

wherein the negative feedback circuit is a series connection circuit in which a first diode and a second diode are connected to each other in series so that their forward directions are opposed to each other, wherein at least one of the first and second diodes have a variable impedance characteristic.

6. (*Previously presented*) The power amplification circuit according to Claim 5, wherein the first and second diodes of the series connection circuit are each constituted of a base-emitter junction or a base-collector junction of one bipolar transistor.

7. (*Original*) The power amplification circuit according to Claim 5, wherein the first and second diodes of the series connection circuit are constituted of a junction between two terminals out of base, collector and emitter of a first bipolar transistor, and a junction between two terminals out of base, collector and emitter of a second bipolar transistor, respectively.

8. (*Original*) The power amplification circuit according to Claim 5, wherein the first and second diodes of the series connection circuit are constituted of a junction between two terminals out of gate, drain and source of a first field effect transistor, and a junction between two terminals out of gate, drain and source of a second field effect transistor, respectively.

9. (*Original*) The power amplification circuit according to Claim 5, wherein the second diode has a junction area larger than the first diode.

10-16. *Canceled.*

17. (*previously presented*) The power amplification circuit according to Claim 4, wherein the impedance of the negative feedback circuit increases with an increasing input signal power to the power amplifier.

18. (*previously presented*) The power amplification circuit according to Claim 5, wherein the impedance of the negative feedback circuit increases with an increasing input signal power to the power amplifier.